

HSM124S

Silicon Epitaxial Planar Diode for High Speed Switching

REJ03G0551-0700 Rev.7.00 Dec 13, 2007

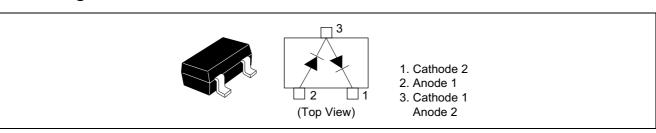
Features

- Low reverse current. ($I_R = 0.01 \mu A \text{ max}$)
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Part No.	Part No. Laser Mark		Package Code	
HSM124S	A1	MPAK	PLSP0003ZC-A	

Pin Arrangement



Absolute Maximum Ratings *1

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Peak reverse voltage	V _{RM}	85	V
Reverse voltage	V _R	80	V
Peak forward current	I _{FM}	300	mA
Non-Repetitive peak forward surge current	I _{FSM} * ²	4	Α
Average rectified current	I ₀	100	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. Per one device

2. Within $1\mu s$ forward surge current.

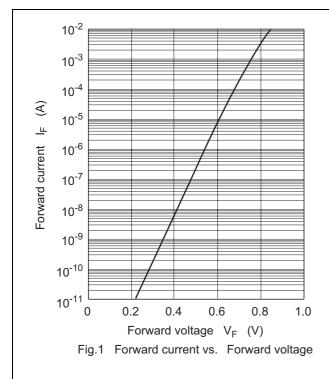
Electrical Characteristics *

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _{F1}	_	_	1.0	V	I _F = 10 mA
	V _{F2}	_	_	1.0	V	I _F = 50 mA
	V _{F3}	_	_	1.2	V	I _F = 100 mA
Reverse current	I _R	_	_	0.01	μА	V _R = 80 V
Capacitance	С	_	_	4.0	pF	V _R = 0 V, f = 1 MHz
Reverse recovery time	t _{rr}	_	_	100	ns	$I_F = 10 \text{ mA}, V_R = 6 \text{ V}, R_L = 50 \Omega$

Note: Per one device.

Main Characteristic



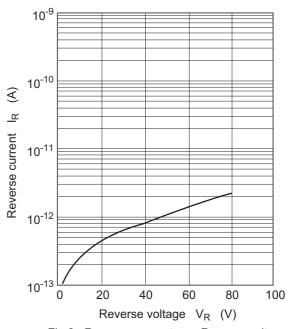
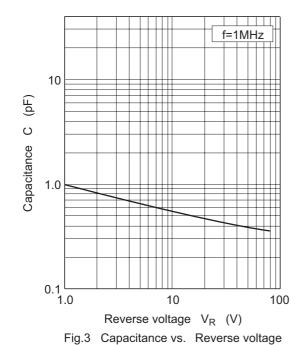
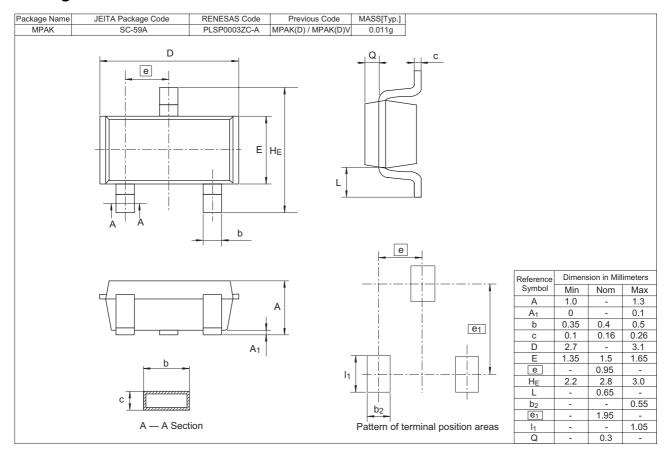


Fig.2 Reverse current vs. Reverse voltage



Package Dimensions



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